

Re: 8051 architecture question

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 - *Date:* Fri, 06 May 2005 05:27:42 GMT
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There are 3 memory spaces (without tricks and such)

1–Code 64K max

2–External RAM 64K max

3–Internal RAM 256 Bytes Max

The internal RAM overlays the 128 bytes of Register Space.

It gets confusing here. You get the full 256 bytes of internal RAM Plus the registers. (there are some overlapping instructions).

The 4 register banks are in the RAM. The bit space (256 bits) over lays part of the RAM and Some of the register. (this allows you to use bit or byte commands on them).

The reserved memory area does nothing, it is extra space to add things in the future.

scratchpad memory is a programming term not an 8051 memory space.

look at www.8052.com for more info.

• *Follow-Ups:*

◆ [**Re: 8051 architecture question**](#)

◇ *From:* Jet Morgan

◆ [**Re: 8051 architecture question**](#)

◇ *From:* Jet Morgan

• *References:*

◆ [**8051 architecture question**](#)

◇ *From:* JBrewster

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