

using of 2-to-4-line Decoder

Source: <http://coding.derkeiler.com/Archive/General/comp.arch.embedded/2005-12/msg00757.html>

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 - *Date:* 28 Dec 2005 01:13:19 -0800
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Hi people ,

I have a logic function:
 $F(A,B,C) = A \text{ XOR } C$

How can I implement it using 2-to-4-line Decoder (74LS156) and NAND gate ?

Thank you in advance.

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- *Follow-Ups:*
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◇ *From:* Chuck F.
 - ◆ [*Re: using of 2-to-4-line Decoder*](#)
◇ *From:* Pete Fenelon
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